	Application No.	Applicant(s)
Notice of Allowability	10/698,260	SRIDEVAN, SRIKANT
	Examiner	Art Unit
	Thinh T. Nguyen	2818
The MAILING DATE of this communication All claims being allowable, PROSECUTION ON THE MERI herewith (or previously mailed), a Notice of Allowance (PTO NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATE of the Office or upon petition by the applicant. See 37 CFR	TS IS (OR REMAINS) CLOSED in DL-85) or other appropriate commu INT RIGHTS. This application is so	this application. If not included nication will be mailed in due course. THIS
1. This communication is responsive to 10/31/2003.		
2. $igtimes$ The allowed claim(s) is/are <u>1-11</u> .		
3. $igotimes$ The drawings filed on <u>31 October 2003</u> are accepted	I by the Examiner.	
4. Acknowledgment is made of a claim for foreign prical algorithms and algorithms and algorithms and algorithms are application number (see 37 each sheet. Replacement sheet(s) should be labeled as sufficient with a factor and a stracked examiner's comment regarding REQUIREM.	s have been received. s have been received in Application rity documents have been received. DATE" of this communication to file DONMENT of this application. submitted. Note the attached EXAch gives reason(s) why the oath or ") must be submitted. ftsperson's Patent Drawing Review———————————————————————————————————	in No in this national stage application from the a reply complying with the requirements MINER'S AMENDMENT or NOTICE OF declaration is deficient. (PTO-948) attached in the Office action of e drawings in the front (not the back) of R 1.121(d). ERIAL must be submitted. Note the
	-948) 6.	ormal Patent Application (PTO-152) Immary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance
Supervisory Patent Examiner Technology Center 2800		

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DETAILED ACTION

Specification

1. The specification has been checked to the extent necessary to determine the presence of all possible minor errors. However, the applicant cooperation is requested in correcting any errors of which the applicant may become aware in the specification.

Reason for allowance

- 2. Claims 1-11 arte allowed. The following is an examiner's statement of reason for allowance: None of the references of record teaches or suggests the claimed ***

 SEMICONDUCTOR DEVICE having the limitations:
 - --"a silicon carbide semiconductor die consists of one of a junction field effect transistor and a metal semiconductor field effect transistor, the silicon carbide semiconductor die having three electrodes including a source electrode, a gate electrode and a drain electrode, each of the three electrodes being on one of a top surface and a bottom surface of the silicon carbide die and the drain electrode being electrically connected to the drain contact; and a silicon metal oxide semiconductor field effect transistor comprising a

doped silicon substrate and three electrodes including a source electrodes

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a gate electrode and a drain electrode, each of the three electrodes being on one of an upper surface and a lower surface of the silicon transistor, the gate electrode of the silicon transistor being electrically connected to the gate contact, the drain electrode of the silicon transistor being electrically connected with the source electrode of the silicon carbide die and the source electrode of the silicon transistor being electrically connected to both the source contact and the gate electrode of the silicon carbide die; "--

and all other limitations as recited in claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Pankove (US patent 6,169,330) discloses a method an apparatus for packaging high temperature solid state electronics device; Parson et al. (US patent Application Publication US 2002/066903 A1 disclose an adhesion and /or encapsulation of silicon carbide based semiconductor device on ceramic substrates.

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thinh T Nguyen whose telephone number is 571-272-1790. The examiner can normally be reached on Monday-Friday 9:00am-6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached at 571-272-1787. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Thinh T Nguyen

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David Nelms
Supervisory Patent Examiner
Technology Center 2800